

10682410_CLS1.txt
Most Frequently Occurring Classifications of Patents Returned
From A Search of 10682410 on June 14, 2005

Original Classifications

6 710/22
4 710/56
3 718/104
2 710/25
2 710/26
2 710/28
2 710/29
2 710/5
2 710/71

710/24 OR
308

710/22 - 28

~~28~~ - 35

52 - 57

208 - 212

111/152 - 154

111 - 113

Cross-Reference Classifications

9 710/22
4 710/36
3 710/113
3 710/240
3 710/28
3 711/152
3 711/154
2 370/229
2 710/10
2 710/100
2 710/104
2 710/25
2 710/33
2 710/35
2 710/37
2 710/40
2 710/52
2 711/100
2 711/146
2 711/151
2 711/163
2 711/202
2 711/205
2 714/3
2 714/30
2 714/4
2 714/43
2 714/56

Combined Classifications

15 710/22
5 710/28
4 710/113
4 710/25
4 710/36
4 710/56
4 718/104
3 710/240
3 710/26
3 711/100
3 711/151
3 711/152
3 711/154
2 370/229
2 709/229
2 710/10
2 710/100

10682410_CLS1.txt

2 710/104
2 710/108
2 710/200
2 710/244
2 710/29
2 710/33
2 710/35
2 710/37
2 710/40
2 710/5
2 710/52
2 710/71
2 710/8
2 711/142
2 711/146
2 711/163
2 711/202
2 711/205
2 714/3
2 714/30
2 714/4
2 714/43
2 714/56



Phos

✓ Highly as far as I can see

owner some buffer some dno

- ⊖ Drafts
 - 🔍 BRS: dma
 - ⊖ Pending
 - == Active
 - 🔍 L1: [0] thread near9 owner near9 buffer near9 dma
 - 🔍 L2: [1] thread same owner same buffer same dma
 - 🔍 L3: [3] owner same buffer same dma
 - 🔍 Failed
 - ⊖ Saved
 - 🔍 S1: [49] [{"S2:2795"} or {"6092127"} or {"5003465"} or {"6006303"} or {"4484273"}]
 - 🔍 S2: [1803] buffer near9 thread
 - 🔍 S3: [3249] buffer near5 pool
 - 🔍 S4: [29] S2 with S3
 - 🔍 S5: [71] dma with S3
 - 🔍 S6: [26] dma with S2
 - 🔍 S7: [94] S5 S6
 - 🔍 S8: [1] [previous past] same {current present} same dma same owner S3
 - 🔍 S9: [113] DMA NEAR9 THREAD
 - 🔍 S10: [2] CHAIN WITH S9
 - 🔍 S12: [498] DMA WITH CHAIN
 - 🔍 S13: [69] BUFFER WITH S12
 - 🔍 S14: [2] THREAD S3 WITH S12
 - 🔍 S15: [1012] [710/22].CCLS.
 - 🔍 S16: [3] S2 and S5
 - 🔍 S17: [116] [710/24].CCLS.
- ⊖ Favorites
- 🔍 Tagged [12]
- 🔍 UDC
- 🔍 Queue
- # Trash

 BBB FORU
 SBB FORU
 Trade
 Taxi
 HOTEL

	U	Doc	Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	
1	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050188129 A1	20050825	35	Facilitating inter-DSP data communications	710/52	711/105		Abdellah
2	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050080943 A1	20050414	11	Method and apparatus for efficient sharing of DMA resource	710/22			Pafumi, J
3	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040149382 A1	20040729	126	Language for handling network packets	709/223			Narad, C
4	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040143655 A1	20040722	91	Accessing transmission control protocol (TCP) segments	709/223			Narad, C
5	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040073738 A1	20040415	15	Scalable efficient I/O port protocol	710/308	711/147		Kessler, R
6	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040042483 A1	20040304	28	System and method for TCP offload	370/463	370/419		Etzur, Uri e
7	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040042458 A1	20040304	28	System and method for handling out-of-order frames	370/394	370/389		Etzu, Uri
8	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030061332 A1	20030327	94	Multiple consumer-multiple producer rings	709/223			Narad, C
9	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030046423 A1	20030306	90	Programmable system for processing a partitioned network infrastructure	709/238			Narad, C
10	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20030005103 A1	20030102	93	Cumulative status of arithmetic operations	709/223	709/201		Narad, C
11	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 20020013896 A1	20020131	18	Multiprocessor system	711/130	711/147		Higuchi, T
12	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6889278 B1	20050503	17	Method and apparatus for fast acknowledgment and efficient servi	710/266	710/261; 710/267		Hoerler, J at
13	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6859841 B2	20050222	83	Programmable system for processing a partitioned network infrastructure	709/236			Narad, C
14	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6738836 B1	20040518	16	Scalable efficient I/O port protocol	710/22	710/105; 710/107;		Kessler, R
15	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6701338 B2	20040302	88	Cumulative status of arithmetic operations	708/525	709/223		Narad, C
16	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6701338 B2	20040302	88	Cumulative status of arithmetic operations	708/525	709/223		Narad, C
17	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6701338 B2	20040302	88	Cumulative status of arithmetic operations	708/525	709/223		Narad, C
18	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6701338 B2	20040302	88	Cumulative status of arithmetic operations	708/525	709/223		Narad, C
19	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6701338 B2	20040302	88	Cumulative status of arithmetic operations	708/525	709/223		Narad, C
20	<input checked="" type="checkbox"/>	<input checked="" type="checkbox"/>	US 6701338 B2	20040302	88	Cumulative status of arithmetic operations	708/525	709/223		Narad, C

EAST - [wsp:1]

File View Edit Tools Window Help

Drafts
Pending
Active
Failed
Saved
Favorites
Tagged (12)
UDC
Queue
Trash

L1: (49) ("5212795" or ("6092127" or ("5003465" or ("6006303" or ("4484273")
L2: (1803) buffer near5 thread
L3: (3249) buffer near5 pool
L4: (29) 2 with 3
L5: (71) dma with 3
L6: (26) dma with 2
L7: (94) 5 6
L8: (1) (previous past) same (current present) same dma same owner33
L9: (113) DMA NEAR9 THREAD
L10: (2) CHAIN WITH 9
L12: (498) DMA WITH CHAIN
L13: (69) BUFFER WITH 12
L14: (2) THREAD33 WITH 12
L15: (1012) (710/22).CCLS
L16: (3) 2 and 5
L17: (116) (710/24).CCLS

US-PG-PUB, USPAT
Default owner: OR
Highlight all full terms matches

BFS form S&P form Image Text HTML

	U		Document ID	Issue Date	Pages	Title	Current OR	Current XRef	Retrieval Class	It
1	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5619720 A	19970408	26	Digital signal processor having link ports for point-to-point communication	710/38			Garde; Daur
2	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6092127 A	20000718	13	Dynamic allocation and reallocation of buffers in links of chained DMA over	710/56	710/22; 710/26;		Tausheck; Er
3	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5828903 A	19981027	19	System for performing DMA transfer with a pipeline control switching such	710/53	709/212; 710/22;		Sethuram; Jo
4	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6324599 B1	20011127	11	Computer system and method for tracking DMA transferred data within	710/26	710/22; 710/24;		Zhou; Ning e
5	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6324598 B1	20011127	12	Software enlarged tag register and method thereof for holding the complet	710/24	710/22; 710/62;		Olson; Steve
6	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5850570 A	19981215	13	Communication apparatus for performing data transfer utilizing direct	710/24	710/17; 710/18;		Shoji; Fumio
7	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5933654 A	19990803	14	Dynamic buffer fracturing by a DMA controller	710/23	710/22		Galdun; Dar
8	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20050033874 A1	20050210	11	Direct memory access using memory descriptor list	710/22			Futrel; William
9	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6718405 B2	20040406	9	Hardware chain pull	710/22	710/24; 710/308;		Rogers; Jeffr
10	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 5781799 A	19980714	16	DMA controller arrangement having plurality of DMA controllers and buffer	710/22			Leger; Gear
11	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 20040267982 A1	20041230	17	Read/write command buffer pool resource management using read-pat	710/52			Jackson; Tho
12	<input type="checkbox"/>	<input checked="" type="checkbox"/>	US 6336150 B1	20020101	30	Apparatus and method for enhancing data transfer rates using transfer contr	710/5	710/18; 710/33;		Ellis; Jackson